

Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(use as many sheets as necessary)</i>				<b>Complete if Known</b>	
				Application Number	(not yet Assigned)
				Filing Date	1/26/2000
				First Named Inventor	KU, et al.
				Group Art Unit	(not yet Assigned)
				Examiner Name	(not yet Assigned)
				Attorney Docket Number	ARC-P002
Sheet	1	of	2		

[illegible][illegible]

1-APR-2003

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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Substitute for form 1449B/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

2

of

2

**Complete if Known**

Application Number	(Not yet Assigned)
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Attorney Docket Number	ARC-P002

09/491389

**OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials <sup>1</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
	1	EISENMANN, H., et al., Genetic Global Placement and Floorplanning, Pps. 269-274, Vol. 17.1, ACM, San Francisco, 1998	
	2	SHAHODKAR, K., et al., VLSI cell Placement Techniques, ACM computing surveys, June 1991, pps. 143-220, Vol. 23, ACM	
	3	FADAVI-ARDEKANI, J., MxN Booth Encoded Multiplier generator using optimized Wallace Trees, IEEE Transactions on Very Large Scale Integration (VLSI) systems, June 1993, pps. 120-123, Vol. 1, No. 2, IEEE	
	4	KIM, J., et al., A Timing-driven Data Path Layout Synthesis with Integer Programming, computer-Aided Design, 1995, pps. 716-719, 1063-6757/95, IEEE	

Examiner  
Signature

Date

Considered

1-APR-2003

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